## IN THE SPECIFICATION

Please amend the specification as follows.

Please replace the following paragraph that begins on page 6, line 5, of the specification and replace with the following:

[[Figure 7 is a simplified block diagram of the microsequencer of Figure 2; and]]

Figure 7 is a simplified block diagram of the microsequencer of Figure 2, furthermore, Figure 7A provides a flowchart illustrating a plurality of steps performed by various apparatus disclosed herein, in accordance with one embodiment of the present invention; and

Please insert the following paragraph on page 23, line 19:

Figure 7A illustrates a flowchart illustrating a plurality of steps performed by the microsequencer 100, in accordance with one embodiment of the present invention. As described above the microsequencer 100 stores instructions in a queue 700, indexed by the instruction pointer 705 (see block 792). The microsequencer 100 may identify a first instruction, marking the start of the first loop, which may be defined by one of the loop counts 720-726 (see block 794). The microsequencer 100 may store the remaining number of iteration for the first loop in a loop counter, illustrated by one of the loop counters 740-746 (block 796). The microsequencer 100 may load the instruction point 705 associated with a subsequent loop instruction pointer value if the number of iterations remaining for the first loop is zero (*i.e.*, when the corresponding loop counter 740-746 equals zero) (block 798).

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